R18

Reg. No:

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)
MCA I Year II Semester Regular Examinations May 2019
COMPUTER ORGANIZATION

Time: 3 hours   Max. Marks: 60

(Answer all Five Units **5 x 12 = 60** Marks)

UNIT-I

1	**a**	Subtract the following with neat diagram. i) 1010100-1000011 ii) 1001001-(-1000100)	6M
	b	Draw the H/W Flowchart for Division	6M

OR

2	**a**	Write about logical operators.	4M
	b	Using K- map simplify the Boolean Fnction F(w, x, y, z)=Σ(0,1,2,4,6,8,9,12,13,14).	8M

UNIT-II

3	Explain about Hardwired Control with the help of a neat diagram.	12M

OR

4	**a**	Write Short notes on Address Sequencing with neat diagram.	6M
	b	Discuss about the cache memory with different types of mappings.	6M

UNIT-III

5	**a**	Write short notes on input-output instructions.	8M
	b	List of different types of IO Instruction.	4M

OR

6	**a**	What is interrupt? Explain about simultaneous request handling by the processor.	6M
	b	Explain about process control instructions.	6M

UNIT-IV

7	**a**	Differentiate between memory mapped I/O and isolated I/O.	7M
	b	List out I/O Interfaces .	5M

OR

8	**a**	Explain about Daisy Chaining.	6M
	b	Explain about Parallel priority.	6M

UNIT-V

9	**a**	Describe briefly about the characteristics of multiprocessors.	6M
	b	Explain about vector processing.	6M

OR

10	**a**	Explain about interprocess Communication.	6M
	b	Explain about Cache Coherence.	6M

*** END ***